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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Tatsuhiro MIZUMASA**

Serial No.: **Not Yet Assigned**

Filed: **March 18, 2002**



For: **SEMICONDUCTOR INTEGRATED CIRCUIT HAVING A PLURALITY OF CIRCUIT
REGIONS WHERE DIFFERENT POWER SUPPLY VOLTAGES ARE USED AND
METHOD OF MANUFACTURING THE SAME**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

March 18, 2002

Sir:

In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and
Trademark Office the reference listed on the attached PTO-1449.

Copy of the reference is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge
Deposit Account No. 01-2340.

Respectfully submitted,
ARMSTRONG, WESTERMAN & HATTORI, LLP

A handwritten signature in black ink, appearing to read "William G. Kratz, Jr.".

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Enclosures: PTO-1449; Reference (1)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 020348	Serial No. New Appln.
	Applicant(s): Tatsuhiro MIZUMASA	
	Filing Date: March 18, 2002	Group Art Unit:

931 U.S. PTO
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)		
_____	AF	11-307639	11/05/99	Japan	Abstract
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OTHER DOCUMENTS

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